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L31	l12 and l18	1	L31
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L29	l12 and l16	0	L29
L28	l12 and l15	6	L28
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L25	l11 and l18	6	L25
L24	l11 and l17	6	L24
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L11	l7 and disjunct$4	364	L11
L10	L9 and (rol$5 or composit$3 or sum$7)	1	L10
L9	5682519.pn. and (cmos or static$6)	1	L9
L8	5682519.pn. and static$6	0	L8
L7	boolean and short$3 and conjunct$4	8055	L7
L6	L4 not l2	4	L6
L5	L4not l2	35	L5
L4	boolean near12 (gate$1 or circuit$4 or path$1) near15 static	39	L4
L3	L2 not l1	6	L3
L2	boolean near8 (gate$1 or circuit$4 or path$1) near12 static	35	L2
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